

Upgrading EVB1000 to latest software IDE

Offset Compensation

Version 1.0

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DOCUMENT INFORMATION

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1 INTRODUCTION

This document is **only** intended for EVK1000 customers who wish to change from default Coocox project and use ST's CubeMX tools and ST System Workbench IDE.

It describes the steps to achieve this port.

Note: It is assumed that user has a knowledge of CubeMX tool and ST System Workbench IDE. For support on these tools ST should be contacted. The versions used in this document:

- STM32Cube MX version 4.18.0
- ST System Workbench Version: Mars.2 Release (4.5.2)



2 CUBEMX CODE GENERATION PROCEDURE

2.1 Installation of tools and drivers

Follow ST's document which describes installation of required tools and drivers [1]. The CubeMX version used for generation of this document is 4.18.0. As a prerequisite STM32F1 firmware package libraries have been installed (version 1.4.0, and all the preceding ones).

2.2 Get EVB1000 schematic

Get EVB1000 schematic from Decawave, this will show you which GPIOs/peripherals are used and how they need to be connected to DW1000 and other components on the EVB1000 board.

2.3 Create CubeMX project

Open STM32CubeMX and start a new project:

- a. Firstly select microcontroller (STM32105RCTx is used on EVB1000)
- b. Then follow steps below to configure various peripherals and GPIOs, as described in 2.4
- c. Then configure the MCU clocks, as described in 2.5
- d. Lastly check the overall configuration, as described in 2.6

2.4 Configure microcontroller peripherals and GPIOs

In the Pinout tab, label and configure the needed GPIOs and their peripheral functions, see Figure 1 below.

- a. For the RCC peripheral select the HSE source as crystal
- b. For the SPI1 peripheral select mode as Full-Duplex master, disable NSS, will use SW controlled CS, thus set GPIO PA4 as GPIO_Output, label as DW_NSS, and the other SPI signals as DW_MOSI, DW_MISO and DW_SCK
- c. For the SPI2 peripheral select mode as Full-Duplex master, disable NSS, will use SW controlled CS, thus set GPIO PB12 as GPIO_Output, label as LCD_NSS, and the other SPI signals as LCD_MOSI, LCD_MISO and LCD_SCK
- d. Configure PB10 and PB11 as GPIO_Output and label as LCD_RW and LCD_RS.
- e. Set GPIOs PC6, PC7, PC8 and PC9 as GPIO_Output these connect to LEDs (LED5, LED6, LED7 and LED8 respectively), label the GPIOs as LED5, LED6, LED7 and LED8.
- f. Configure GPIO PA0 as EXTI0, and label as DW_RESET
- g. Configure GPIO PB5 as EXTI5, and label as DW_IRQn
- h. Configure GPIO PB0 as GPIO_Output and label as DW_WUP
- i. In the *MiddelWares* section enable USB_DEVICE and select Communications Device Class (Virtual Port Com) as Class for FS IP, this will configure PA12, PA11 and PA9 GPIOs, note in the USB_OTG_FS peripheral select Device_Only and activate VBUS. Label PA9, PA11 and PA12 as USB_VBUS, USB_DM and USB_DP, also select PA10 as GPIO_Input and label as USB_ID.
- j. Configure PB3 as JTDO, PB4 as JTRST, PA13 as JTMS, PA14 as JTCK and PA15 as JTMS. Label the pins as J_TDO, J_TRST, J_TMS, _JTCK and J_TMS respectively.
- k. Configure PB2 as GPIO_Input and label as PB2_BOOT1
- I. Configure PC0, PC1, PC2, PC3, PC4 and PC5 and GPIO_Input and label as PC0, PC1, PC2, PC3, PC4 and PC5 respectively.





Figure 1: STM32 pinout after configuration

2.5 Check clock configuration

We would like the HCLK to be at 72 MHz and the USB to be at 48 MHz. To achieve this firstly set Input frequency to match the oscillator on the EVB1000, set this to 12 MHz, see Figure 2. Then Set USB to 48 MHz, and finally set APB2 clock to 72 MHz.





Figure 2: Clock configuration







2.6 Configuration Tab

a. SPI1 change the prescaler 32 to give 2.25 MBits/s, the polarity should be low and phase set to 1st edge. In the GPIO settings set MISO to Pull-down. Other settings can be left at defaults.

Paramete	r Settings 🗹 L	Jser Constants	NVIC Setting	ıs 🗹 DMA Set	tings 🗹 GPIO	Settings	
Search Signa Search (Crt	ls (+F)					Show only	Modified Pins
Pin Name	Signal on Pin	GPIO outpu	GPIO mode	GPIO Pull-u	Maximum o	User Label	Modified
PA5	SPI1_SCK	n/a	Alternate Fu	n/a	High	DW_SCK	
PA6	SPI1_MISO	n/a	Input mode	Pull-up	n/a	DW_MISO	
PA7	SPI1_MOSI	n/a	Alternate Fu	n/a	High	DW_MOSI	
? Select Pins	from table to con	nfigure them. Mu	ltiple selectio	n is Allowed.			

Figure 4: SPI1 configuration

b. SPI2 change the prescaler to 128 to give 281.25 Kbits/s, the polarity should be high and phase set to 2nd edge.

Paramete	r Settings 🛛 🛷 L	lser Constants	🖋 NVIC Setting	gs 🥜 DMA Set	tings 🗹 GPIO	Settings	
Search Signa	l s +F)					Show only	Modified Pins
Pin Name	Signal on Pin	GPIO outpu	GPIO mode	GPIO Pull-u	Maximum o	User Label	Modified
B13	SPI2_SCK	n/a	Alternate Fu	n/a	High	LCD_SCK	
314	SPI2_MISO	n/a	Input mode	No pull-up an	n/a	LCD_MISO	\checkmark
15	SPI2_MOSI	n/a	Alternate Fu	n/a	High	LCD_MOSI	\checkmark

Figure 5: SPI2 configuration

- c. GPIOs:
 - Set PB5 as Pull-down
 - Set PB0 as Open drain
 - Set all Output GPIOs to have high max output speed



• Set PB0, PA4 and PB12 output to High

bearerroignais							
Search (Crtl+	-F)					Show on	ly Modified Pins
Pin Name	Signal on Pin	GPIO output I	GPIO mode	GPIO Pull-up/	Maximum out	User Label	Modified
PAO-WKUP	n/a	n/a	External Interr	No pull-up and	n/a	DW_RESET	\sim
PA4	n/a	High	Output Push Pull	n/a	High	DW_NSS	
A10	n/a	n/a	Input mode	No pull-up and	n/a	USB_ID	\checkmark
² B0	n/a	High	Output Open	n/a	High	DW_WUP	
PB2	n/a	n/a	Input mode	No pull-up and	n/a	PB2_BOOT1	\checkmark
×B5	n/a	n/a	External Interr	Pull-down	n/a	DW_IRQn	
PB 10	n/a	Low	Output Push Pull	n/a	High	LCD_RW	\checkmark
B11	n/a	Low	Output Push Pull	n/a	High	LCD_RS	
B12	n/a	High	Output Push Pull	n/a	High	LCD_NSS	\checkmark
°C0	n/a	n/a	Input mode	No pull-up and	n/a		
C1	n/a	n/a	Input mode	No pull-up and	n/a		
PC2	n/a	n/a	Input mode	No pull-up and	n/a		
°C3	n/a	n/a	Input mode	No pull-up and	n/a		
PC4	n/a	n/a	Input mode	No pull-up and	n/a		
C5	n/a	n/a	Input mode	No pull-up and	n/a		
PC6	n/a	Low	Output Push Pull	n/a	High	LED5	\checkmark
PC7	n/a	Low	Output Push Pull	n/a	High	LED6	
PC8	n/a	Low	Output Push Pull	n/a	High	LED7	\checkmark
6.04	n/a	Low	Output Push Pull	n/a	High	LED8	

Figure 6: GPIO configuration

- d. NVIC:
 - Select EXTI line0 interrupt, and set priority to 3
 - Select EXTI line[9:5] interrupts and set priority to 4
 - Set USB interrupt priority to 7
 - Set tick timer priority to 5
 - Select EXTI0, EXTI[9:5] and USB for init seq. ordering, the rank does not matter.

INVIC Con	figuration				\times
NVIC 🗹	Code generation				
Priority Group	4 bits for pre-emption priority 0 bits for	subpriority \lor	Sort by Premption	Priority and Sub Pro	ority
Search	Search (Crtl+F)		Show only enable	d interrupts	
Interrupt Table		Enabled	Preemption Priority	Sub Priority	
Non maskable in	terrupt	~	0	0	~
Hard fault interr	upt		0	0	
Memory manage	ment fault	\checkmark	0	0	
Prefetch fault, n	nemory access fault		0	0	
Undefined instru	uction or illegal state	~	0	0	
System service	call via SWI instruction	\checkmark	0	0	
Debug monitor		\checkmark	0	0	
Pendable reque	st for system service	~	0	0	
Time base: Syst	em tick timer	\checkmark	5	0	
PVD interrupt th	rough EXTI line 16		0	0	
Flash global inte	rrupt		0	0	
RCC global inter	rupt		0	0	
EXTI line0 interr	upt	\checkmark	3	0	
EXTI line[9:5] in	terrupts	\checkmark	4	0	
SPI1 global inter	rupt		0	0	
SPI2 global inter	rupt		0	0	
USB OTG FS glo	bal interrupt	\checkmark	7	0	
					~
	Enabled F	Preemption Priority	Sub Priority	~	
			Apply	Ok Cance	I

Figure 7: NVIC configuration



2.7 Project settings

In the Project Settings configuration:

- a. Assign project name e.g. EVK1000_CubeMX, and select location, i.e. where the code will be generated
- b. Select Toolchain / IDE set as SW4STM32
- c. Select "Generate Under Root"
- d. In the Code Generator tab: HAL Settings set all free pins as analog

Project Settings		
ject Code Generator A	dvanced Settings	
Project Settings		
Project Name		
EVK1000_CubeMX		
Project Location		
C:\Software\SoftwareRep	00\dw_evk1000	
Toolchain Folder Location		
C: Software Softwarekep	20/gw_eAk1000/EAk1000_C0D6WX/	
Toolchain / IDE		
SW4STM32	✓ Generate Under Root	
Minimum Stack Size	0x400	
Mau and Firmware Package		
Mcu Reference	-	
STM32F105RCTx		
Firmware Package Name a	nd Version	
STM32Cube FW_F1 V1.4.	0	
Use Default Firmware	Location	_
	ube/Repository/STM32Cube FW F1 V1.4.0	Browse
C:/Users/zskrba/STM32Cu		
C:/Users/zskrba/STM32Cu		
C:/Users/zskrba/STM32O		

Figure 8: Project Settings

2.8 Generate Code

Once you reach this step all the project settings should be configured. Save the project and then generate the code. Figure 9 shows the generated folder structure and files.



Drivers	06-Dec-16 2:42 PM	File folder	
nc Inc	06-Dec-16 2:42 PM	File folder	
Middlewares	06-Dec-16 2:42 PM	File folder	
Src Src	06-Dec-16 2:42 PM	File folder	
cproject	06-Dec-16 2:42 PM	CPROJECT File	12 KB
📄 .mxproject	06-Dec-16 2:42 PM	MXPROJECT File	6 KB
📄 .project	06-Dec-16 2:42 PM	PROJECT File	1 KB
EVK1000_CubeMX.ioc	06-Dec-16 2:42 PM	STM32CubeMX	9 KB
💦 EVK1000_CubeMX.xml	06-Dec-16 2:42 PM	nRFgoStudio.nRF8	1 KB
STM32F105RCTx_FLASH.Id	06-Dec-16 2:42 PM	LD File	6 KB

Figure 9: Folder containing the generated project files and code



3 BUILDING SIMPLE APPLICATION IN ST WORKBENCH

Import newly generated project into ST system Workbench IDE. Go to File->Import-> and select the folder where the project has been generated.

C/C++ - DW1000_API_CubeMX/Src/appli	cation/dw_main.c - Eclipse	e Import -	
File Edit Source Refactor Navigate New Open File	Search Project Run Windc Alt+Shift+N >	² Select Create new projects from an archive file or directory.	Ľ
Close Close All	Ctrl+W Ctrl+Shift+W	L Select an import source:	
Save Save As	Ctrl+S	Existing Projects into Workspace	
Revert Move	Ctri+Shift+S 5	a >	
Rename	F2 F5	 >	
Print	Ctrl+P	>	
Switch Workspace Restart	> (> 🍃 Iracing	
🔄 Import		a	
Properties	Alt+Enter	a ? < Back Next > Finish	Cano

Figure 10: Importing newly generated project

Then in the project settings select internal builder, and build the project by clicking the build icon

To download to the EVB1000, press , and select Ac6 STM32 Application, then select Debug device (e.g. ST-LinkV2) and download to the EVB1000.

Once the project can be built successfully and downloaded to the EVB1000, you can also visually check that the code is running, by adding following two lines into main while (1) loop:

```
while (1)
{
    /* USER CODE END WHILE */
        HAL_GPIO_TogglePin(LED5_GPIO_Port, LED5_Pin);
        HAL_Delay(1000);
    /* USER CODE BEGIN 3 */
}
```

This will flash LED5 on the EVB1000.



4 PORTING EVK APPLICATION INTO SYSTEM WORKBENCH IDE

Section above showed how to build and download simple application into EVB1000 HW. Next the user can port Decawave's EVK application.

- 1. Copy application files from EVK1000 Src\application folder into Src\application
- 2. Copy compiler file from EVK1000 Src\compiler folder into Src\compiler
- 3. Copy decadriver files from EVK1000 Src\decadriver folder into Src\decadriver
- 4. Copy deca_usb.h and deca_usb.c files from EVK1000 Src\usb folder into Src\usb Some of the CubeMX generated files which contain the library interface functions need to be modified: usbd_cdc_if.c, also deca_usb.h and deca_usb.c need to be modified to work with new HAL API. Please refer to the provided EVK1000 ST Workbench project.
- 5. Similarly the platform dependant files found in EVK1000 Src\platform need to be adapted for the new HAL. Especially SPI and other GPIO functions in port.h For this aging please refer to the provided EVK1000 ST Workbench project.
- 6. Add a call to dw_main() in the main.c

```
/* USER CODE BEGIN 0 */
extern void dw_main(void);
/* USER CODE END 0 */
while (1)
{
/* USER CODE END WHILE */
        dw_main();
/* USER CODE BEGIN 3 */
}
You are new reaching entry.
```

- 7. You are now ready to compile and build Decaranging EVK1000 code.
- Note: In the project properties, C/C++ Build settings, in GCC Linker a specs=nano.specs option should be removed to allow floating point printing to e.g. LCD.

4.1 Conclusion

This document gives the basic overview of how to use ST's tools (Cuble MX and System Workbench) and Decawave's EVB1000 HW to create applications on the EVB1000. This is a good starting point on which further application scan be developed.



5 REFERENCES

5.1 Listing

References are made to the following documents in the course of this document:

Table 1: Table of References

Ref	Author	Version	Title
[1]	Decawave	Current	EVK1000 User Manual

6 DOCUMENT HISTORY

6.1 Revision History

Table 2: Document History

Revision	Date	Description
1.0	15-Feb-2017	Initial release

7 MAJOR CHANGES

Revision 1.0

Page	Change Description
All	Initial Release



8 ABOUT DECAWAVE

Decawave is a pioneering fabless semiconductor company whose flagship product, the DW1000, is a complete, single chip CMOS Ultra-Wideband IC based on the IEEE 802.15.4-2011 UWB standard. This device is the first in a family of parts that will operate at data rates of 110 kbps, 850 kbps and 6.8 Mbps.

The resulting silicon has a wide range of standards-based applications for both Real Time Location Systems (RTLS) and Ultra Low Power Wireless Transceivers in areas as diverse as manufacturing, healthcare, lighting, security, transport, inventory & supply chain management.

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